



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group art unit: 2815  
Examiner: Edward Joseph Wojciechowicz  
Confirmation number: 4138

In Re PATENT APPLICATION of:

Applicant(s): Katsuhiro HISAKA

Serial No.: 10/673,392

Filing Date: September 30, 2003

For: FUSE CIRCUIT AND SEMICONDUCTOR  
DEVICE INCLUDING THE SAME

Atty. Ref.: IIZ 138

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) **AMENDMENT**  
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March 11, 2008

**Mail Stop AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Examiner's Action mailed on December 12, 2007, please  
amend the above-identified application as follows: